

Practitioner's Docket No. MI22-1921

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CENTRAL FAX CENTER
MAR 29 2005 PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Tran, Luan C.

Application No.: 10/071,453
Filed: 02/08/02

Group No.: 2813
Examiner: L.M. Schillinger

For: Semiconductor Processing Methods of Forming Transistors, Semiconductor Processing Methods of Forming Dynamic Random Access Memory Circuitry, and Related Integrated Circuitry

Commissioner for Patents
Washington, D.C. 20231

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Supplemental Information Disclosure Statement
PTO-Form 1449

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CENTRAL FAX CENTER

MAR 29 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/071,453
Filing Date February 8, 2002
Inventor Luan C. Tran
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner L.M. Schillinger
Attorney's Docket No. MI22-1921
Customer No. 021567

Title: A Plurality of Transistors Having Different Active Area Widths and Different Threshold Voltages Defined by STI

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References - - See attached Form PTO-1449

The attached Form PTO-1449 is submitted in compliance with 37 C.F.R. §§ 1.56. Pursuant to Federal Register Vol. 69, No. 182, pg. 56542 (September 21, 2004), no copies of any cited U.S. patents or U.S. published applications are included herewith. Copies of all other cited art references, if any, are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of these references is respectfully requested.

Respectfully submitted,

Date: 3-29-05

D. Brent Kenady
Reg. No. 40,045

Sheet 1 of 1

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-1921		SERIAL NO. 10/071,433	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Luan C. Tran			
				FILING DATE February 8, 2002		GROUP 2813	
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,834,019	12/04	Tran et al.			
	AB	6,734,487	05/04	Tran et al.			
	AC	6,228,713	05/01	Pradeep et al.			
	AD	6,501,114	12/02	Cho et al.			
	AE	6,620,704	09/03	Miura et al.			
	AF	6,436,751	08/02	Liou et al.			
	AG	6,770,927	08/04	Cho et al.			
	AH						
	AI	2004/0188777A1	09/04	Hwang			
	AJ	2004/0214355A1	10/04	Miura et al.			
	AK						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
	AL						
	AM						
	AN						
	AO						
	AP						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
	AR						
	AS						
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EXAMINER				DATE CONSIDERED			
<small>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>							